

OPEN EDGE 2U BACKPLANE

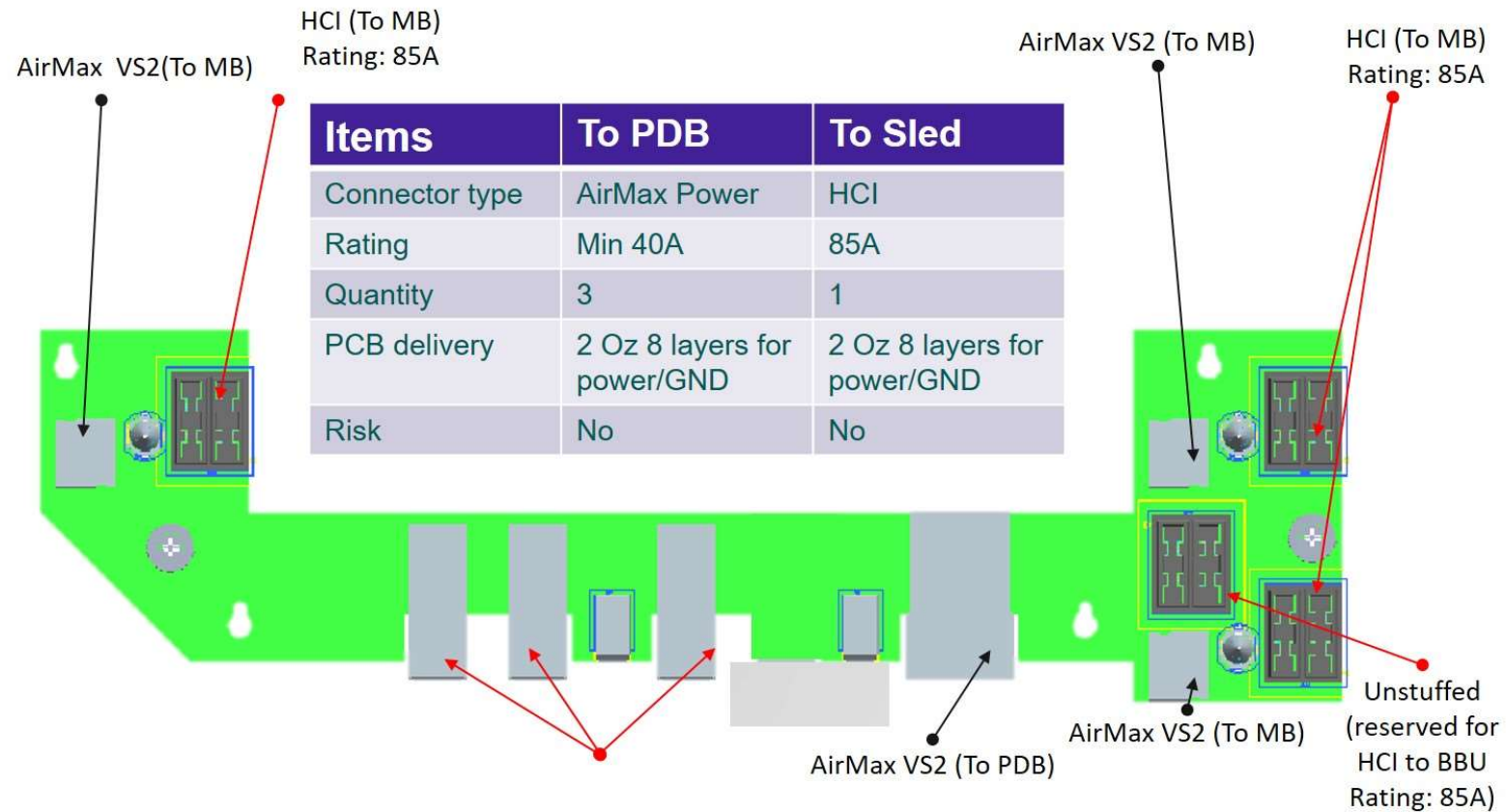
FAB: 1

REV: V1.00
DATE: 2019/07/23

PCB: DAS9NQIB8A0
PCBA: 37S9NIB0000

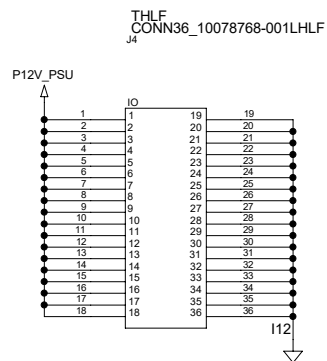


DEPARTMENT	DESIGNER	PROJECT	DOCUMENT NUMBER	REV
	REVIEWER	OPEN EDGE 2U BP	N/A	1.00
SIZE C		DATE Thu Sep 19 13:54:33 2019	SHEET 1 OF 24	

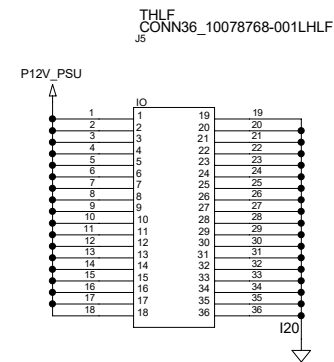


FABX 1.00

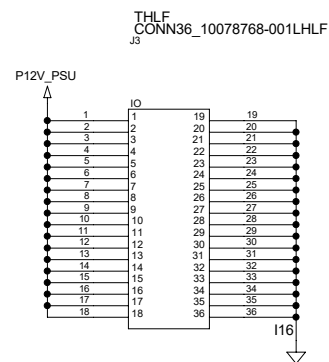
TO SLED2



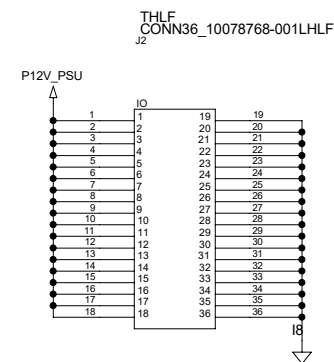
TO SLED3



TO BBU

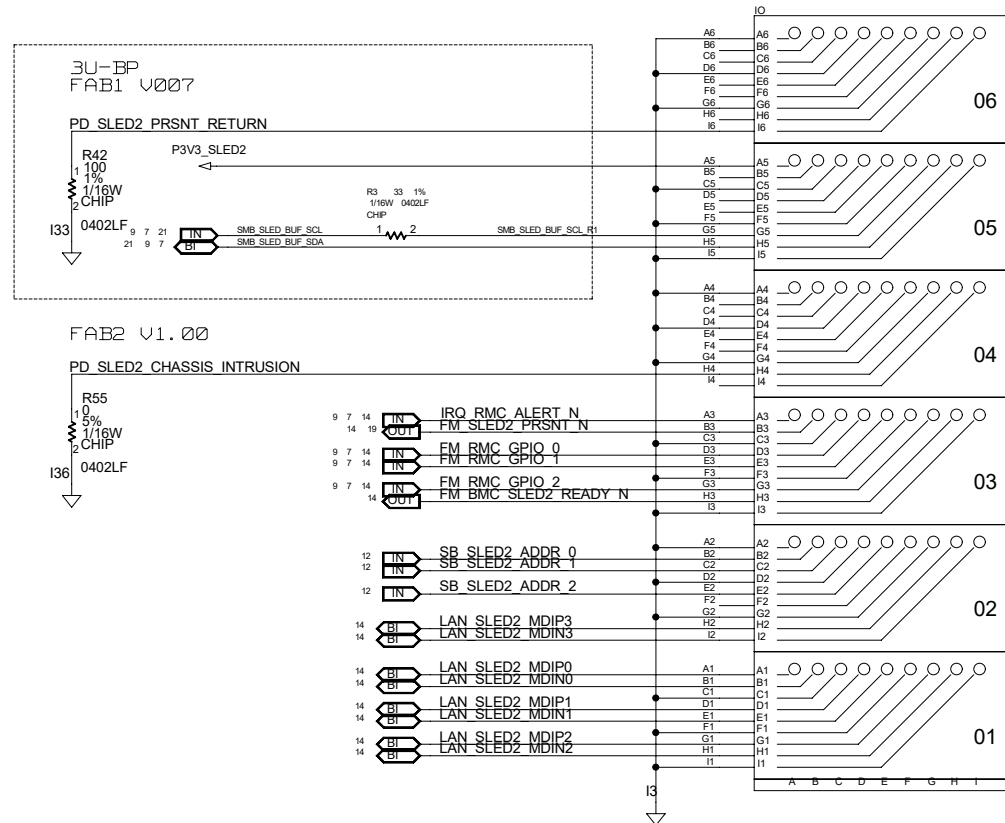


TO SLED1/BBU



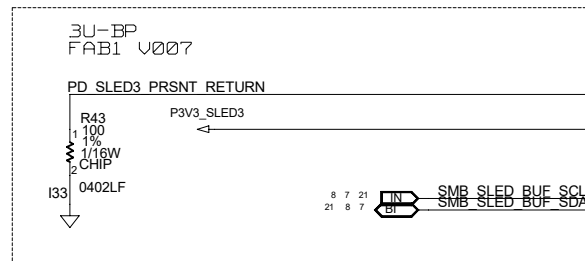
TO SLED2

THLF
CONN54_10130665-102LF
J9



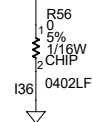
TO SLED3

THLF
CONN54_10130665-102LF
J10



FAB2 V1.00

PD SLED3 CHASSIS INTRUSION



8 7 21 IN SMB SLED3 BUF SCL
21 8 7 BI SMB SLED3 BUF SDA

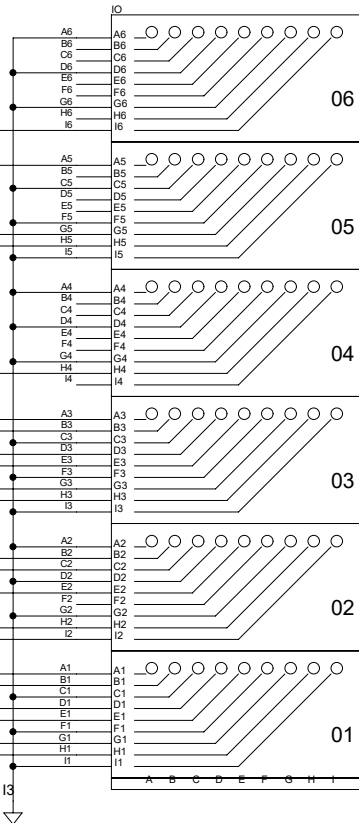
14 8 7 IN JRC RMC ALERT_N
14 19 OUT FM SLED3 PRSNT_N
8 7 14 IN FM RMC GPIO 0
8 7 14 IN FM RMC GPIO 1
8 7 14 IN FM RMC GPIO 2
14 14 OUT FM BMC SLED3 READY_N

12 IN SB SLED3 ADDR 0
12 IN SB SLED3 ADDR 1
12 IN SB SLED3 ADDR 2

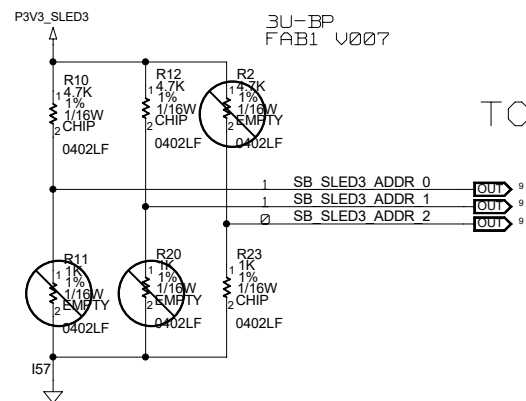
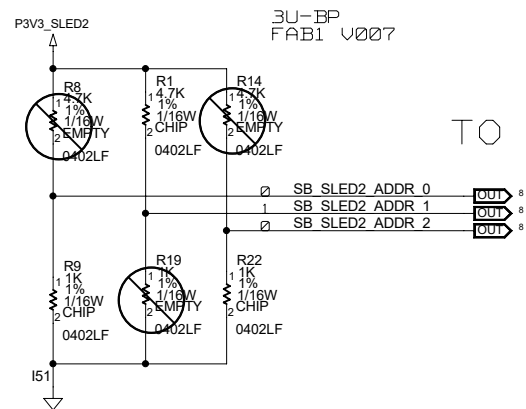
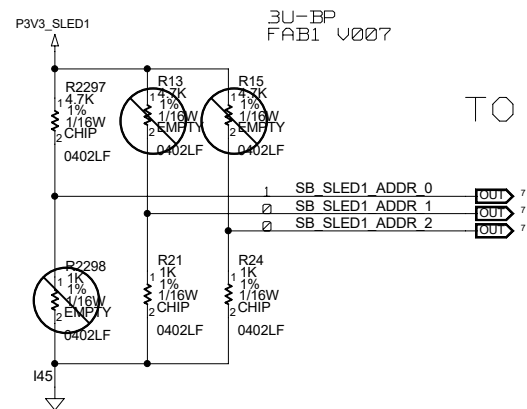
14 BI LAN SLED3 MDIP3
14 BI LAN SLED3 MDIN3

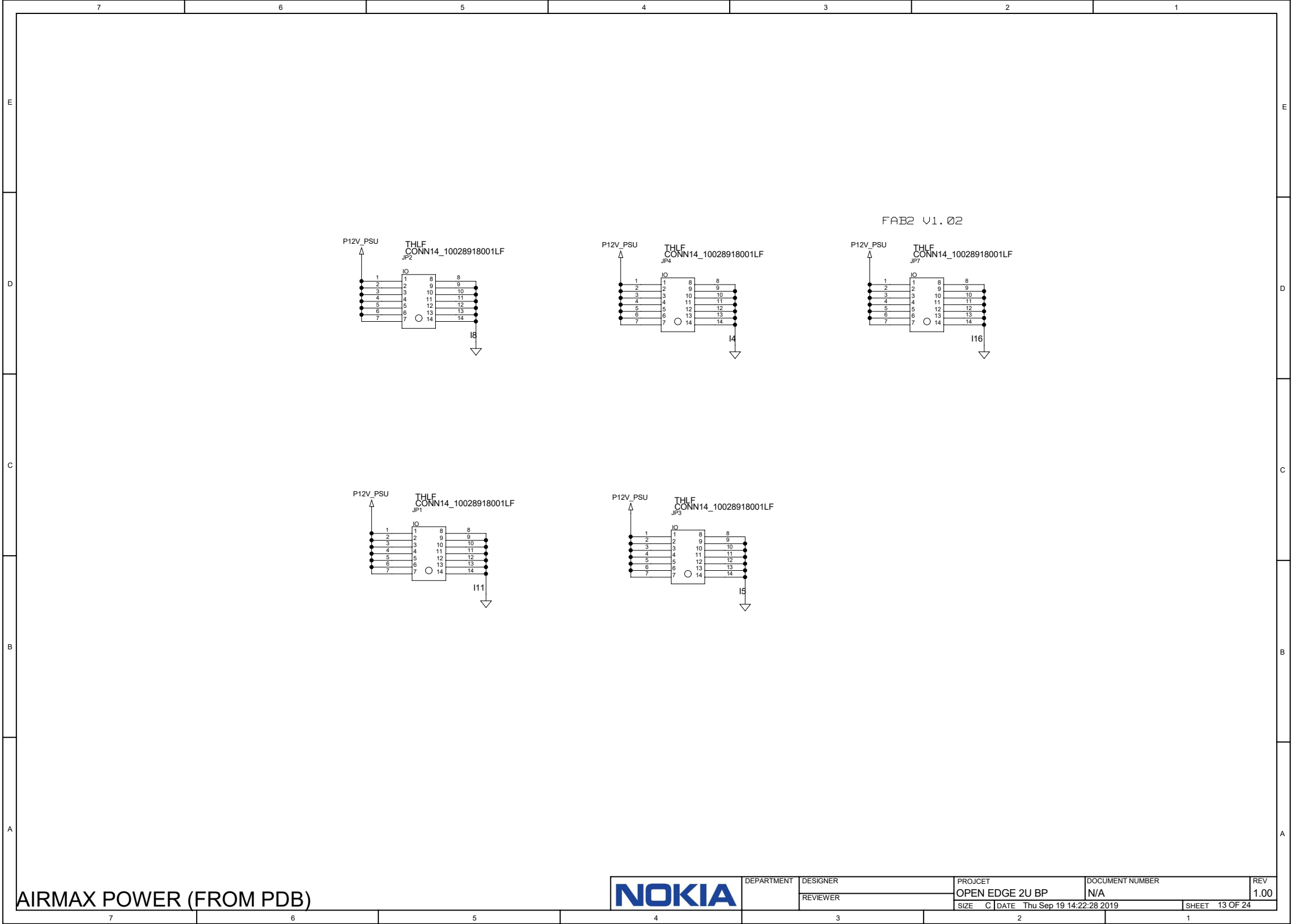
14 BI LAN SLED3 MDIP0
14 BI LAN SLED3 MDIN0
14 BI LAN SLED3 MDIP1
14 BI LAN SLED3 MDIN1

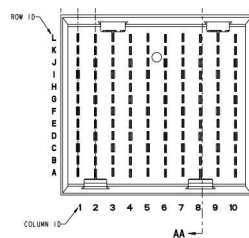
14 BI LAN SLED3 MDIP2
14 BI LAN SLED3 MDIN2



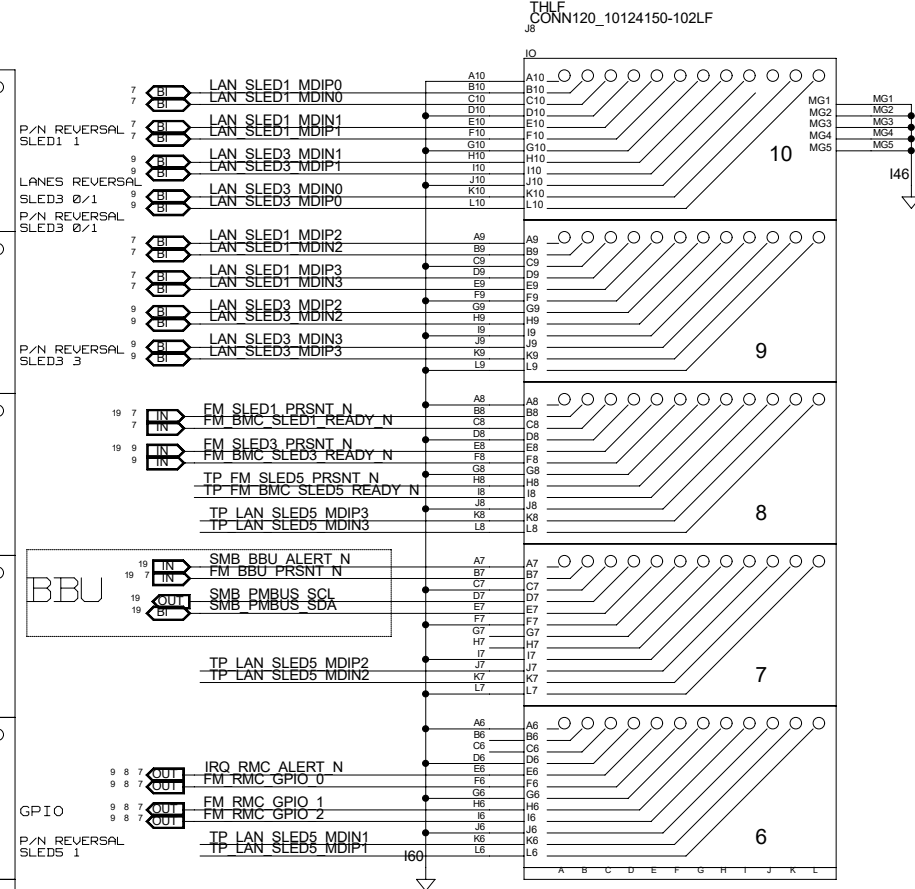
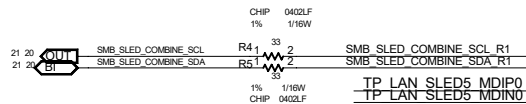
DEPARTMENT	DESIGNER	PROJECT	DOCUMENT NUMBER	REV
	REVIEWER	OPEN EDGE 2U BP	N/A	1.00
SIZE	C	DATE	Thu Sep 19 14:22:27 2019	SHEET 9 OF 24







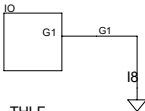
FROM PDB



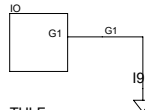
DESIGN NOTE:

DETAIL [B] * 3PCS

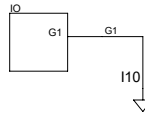
THLF
CONN1_D11402-200000-Z1
J37



THLF
CONN1_D11402-200000-Z1
J39



THLF
CONN1_D11402-200000-Z1
J40



E

D

C

B

A

E

D

C

B

A

7

6

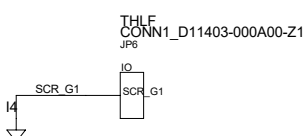
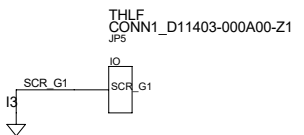
5

4

3

2

1



7

6

5

4

3

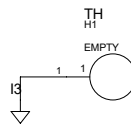
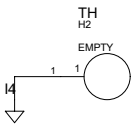
2

1

7	6	5	4	3	2	1
E						
D						
C						
B						
A						

DESIGN NOTE:

DETAIL[C] * 2PCS



BOM NOTE:

DESIGN NOTE:

FBF0626.3010 * 2 PCS
NODE PDB THUMB SCREW S2P(FBS2P096;.3D)BLUE

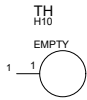
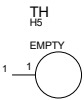
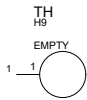
THUMB SCREW



DEPARTMENT	DESIGNER	PROJECT	DOCUMENT NUMBER	REV
	REVIEWER	OPEN EDGE 2U BP	N/A	1.00
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		2		1

DESIGN NOTE:

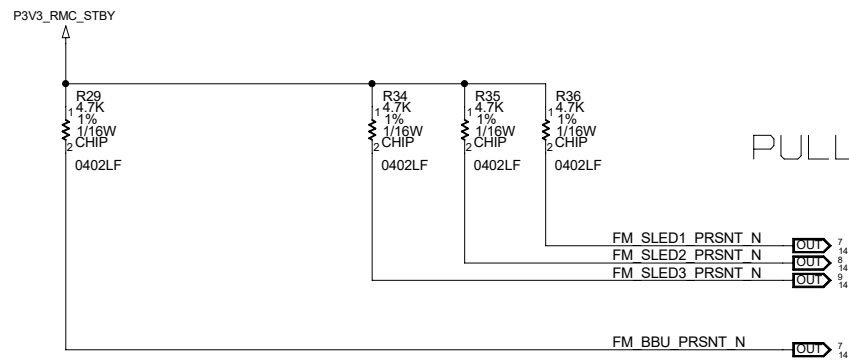
DETAIL[A] * 6PCS



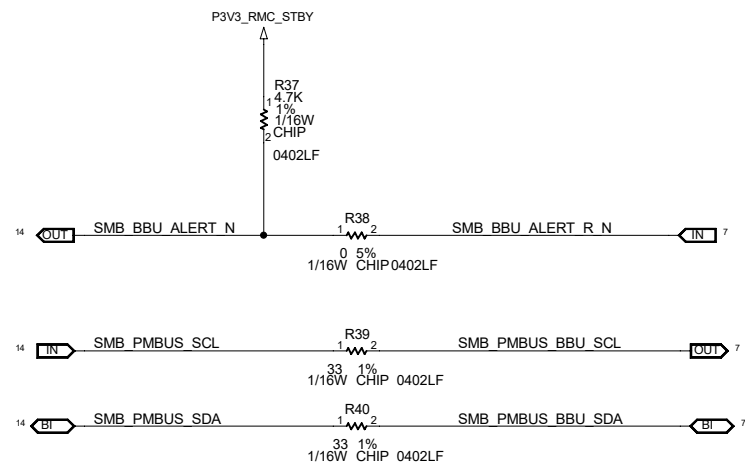
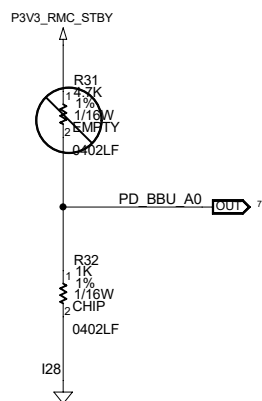
HOLE



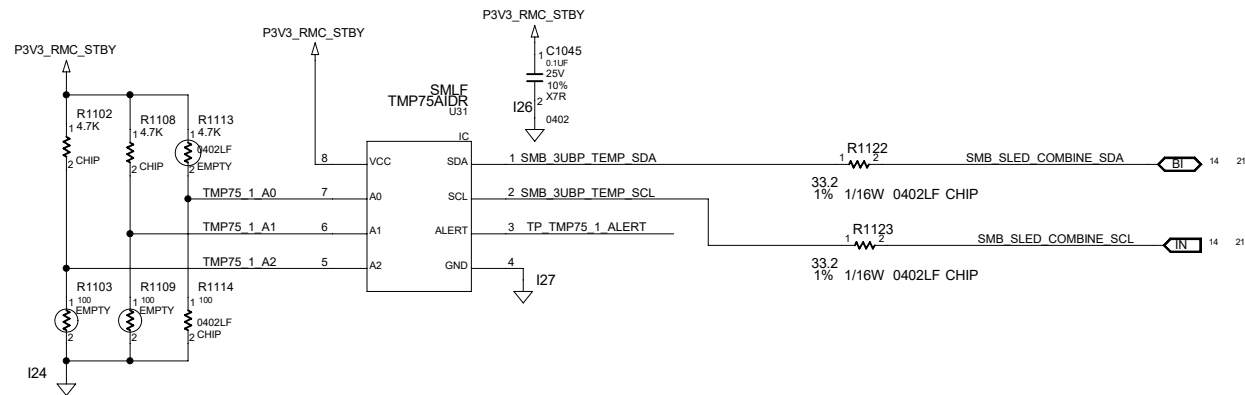
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	REVIEWER	OPEN EDGE 2U BP	N/A	1.00
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TO BBU ADDRESS

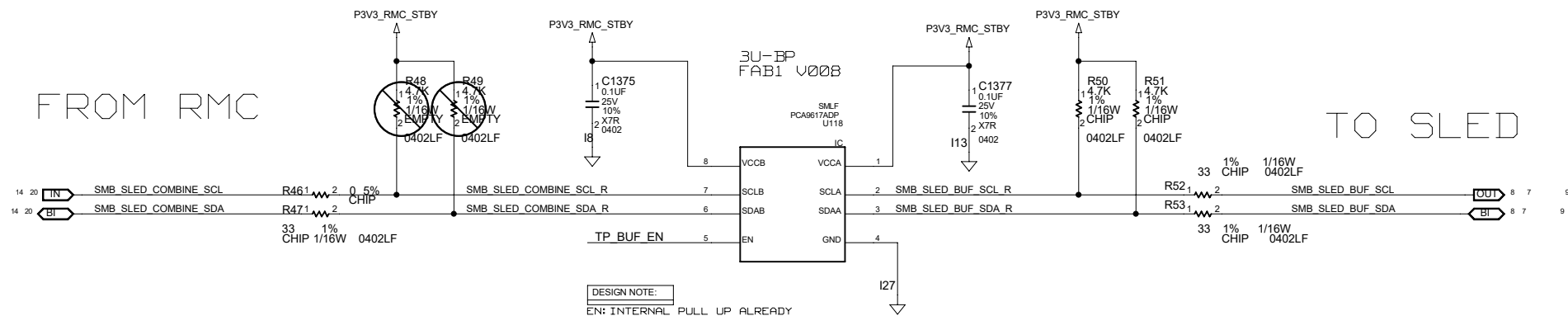


SMBUS ADDRESS: 0X9C THERMAL SENSOR ADDRESS : 10011100



DESIGN NOTE: PCA9617
ENABLE PIN(MIN):0.3VCC(B)
PORT A:VIH 0.7VCC(A)-VCC(A)
LOW VOL SIDE VIL:-0.5-0.45V
VOL:-0.2V

PORT B:VIH 0.7VCC(B)-VCC(B)
HIGH VOL SIDE VIL:-0.5-0.4V
VOL:0.47-0.6V



MECH
WEEE
DM2

EMPTY
DUMMY_SYMBOL¹

MECH
PB-E1_SMALL
DM3

EMPTY
DUMMY_SYMBOL

MECH
PCBA_LABEL_15X5
DM4

MECH

DUMMY_SYMBOL

MECH
BARCODE_20X6
DM8

MECH

DUMMY_SYMBOL

MECH
BARCODE_20X6
DM7

MECH
DUMMY_SYMBOL

MECH
PCB_VENDOR_LOGO_15X8
DM6

EMPTY
DUMMY_SYMBOL

TH
H13
EMPTY
1 — 1

TH
H14
EMPTY
1 — 1

FAB2 V1.01

3U-BP
FAB1 V009

NOKIA

DEPARTMENT

T	DESIGNER
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REVIEWER

PROJCET

— OPEN EDGE 2U BP

SIZE	C	DATE	Thu Sep 19 14:22:24 2019
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DOCUMENT NUMBER

	N/A
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SHEET 22 OF 24

REV

1.00

DUMMY SYMBOL

BOM NOTE:

MS30050I070 *2PCS SCREW_M30L50_NWI
MM30040BCI5 *3PCS SCREW_M30L40_NWB

NOKIA

DEPARTMENT

T	DESIGNER
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REVIEWER

PROJCET

OPEN EDGE 2U BP

DOCUMENT NUMBER

N/A

REV	
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1.00

SIZE	C	DATE	Thu Sep 19 14:22:31 2019	SHEET	24 OF 24
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2

1

SCREW HOLE & LABEL